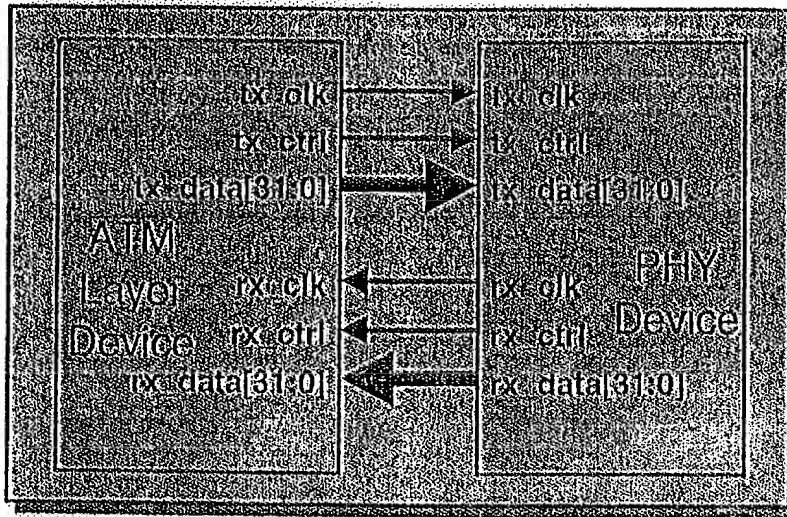
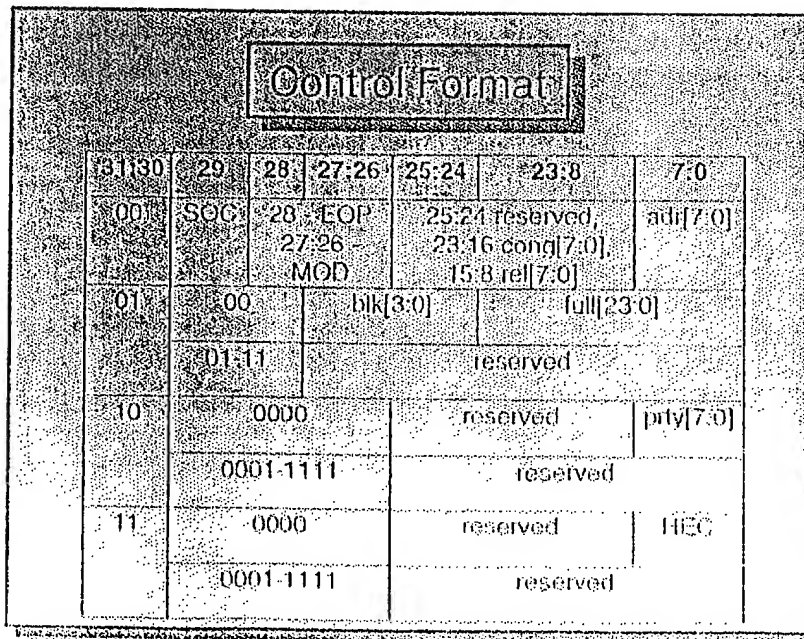


Fig. 1



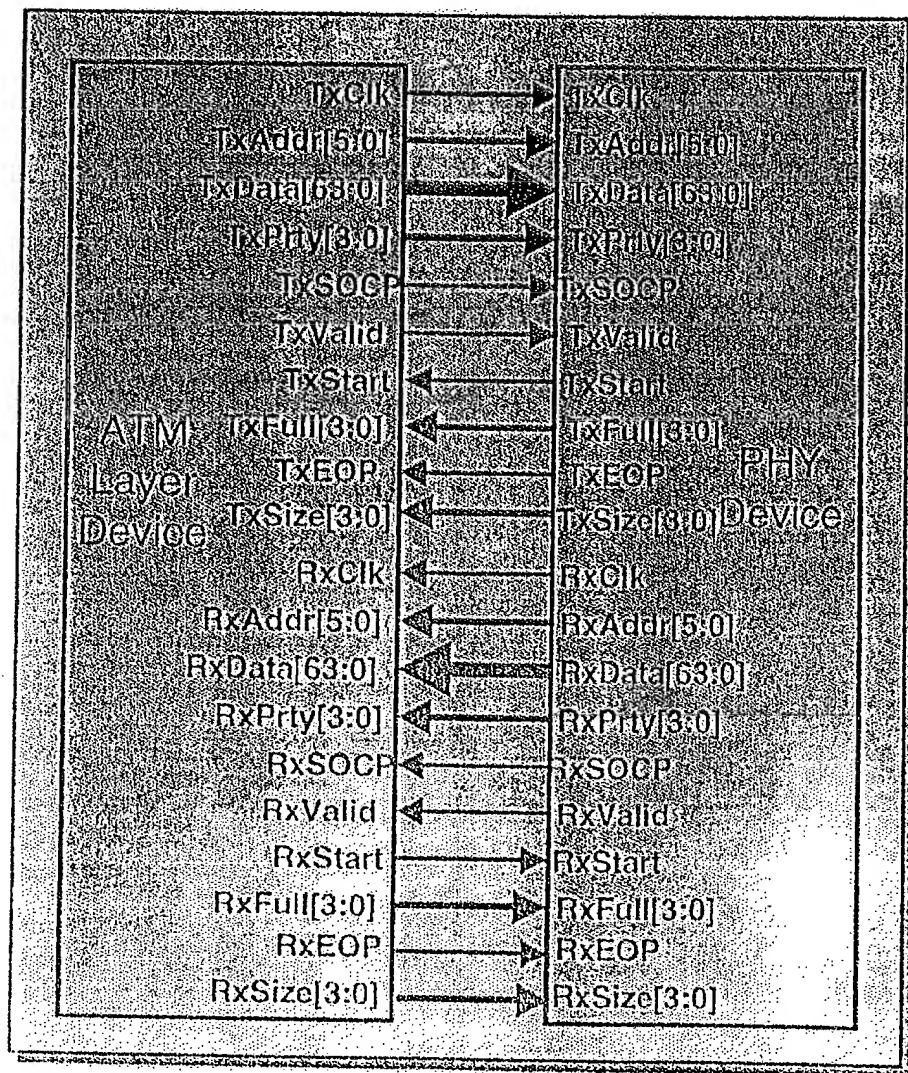
PRIOR ART

Fig. 2



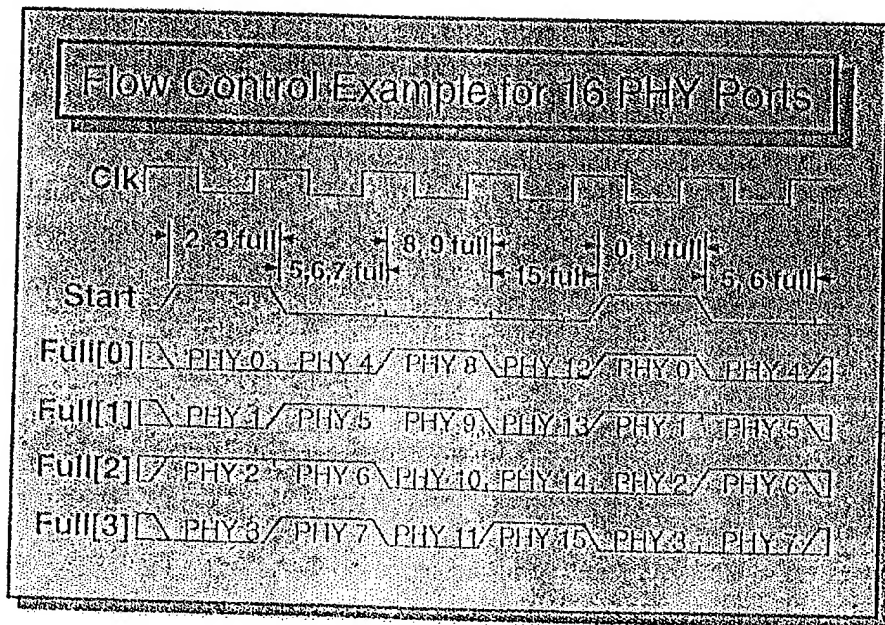
PRIOR ART

Fig. 3



PRIOR ART

Fig. 4.



PRIOR ART

Fig. 5

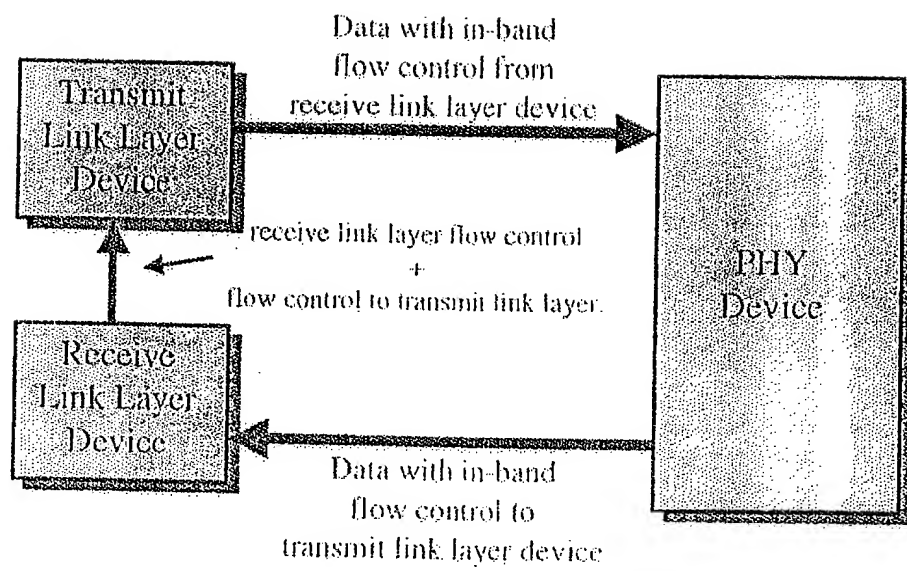


Fig. 6

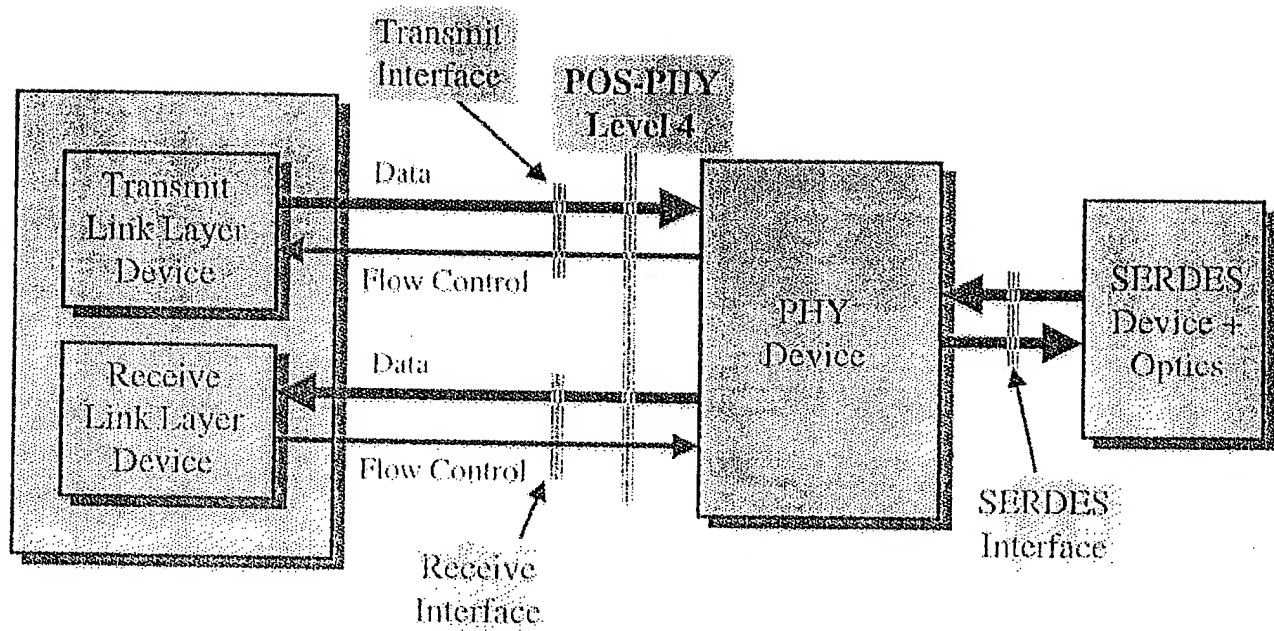


FIG. 7

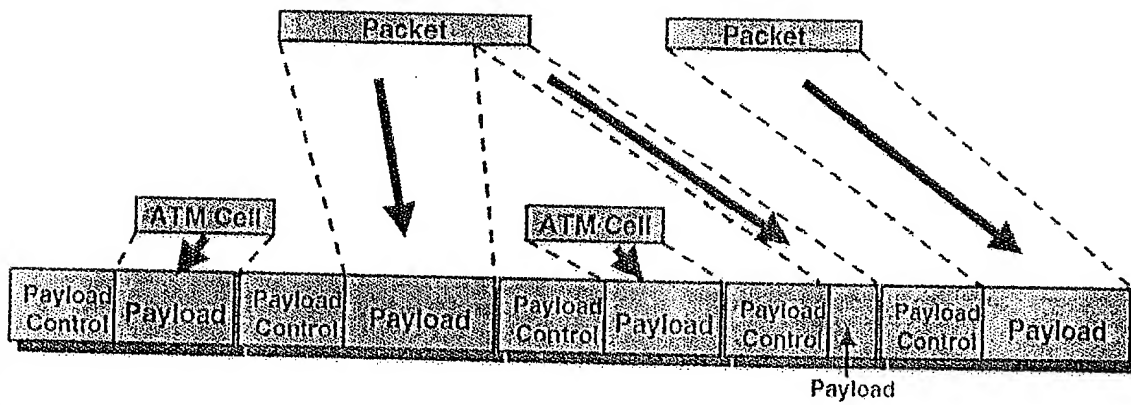


FIG. 8

FIG. 8

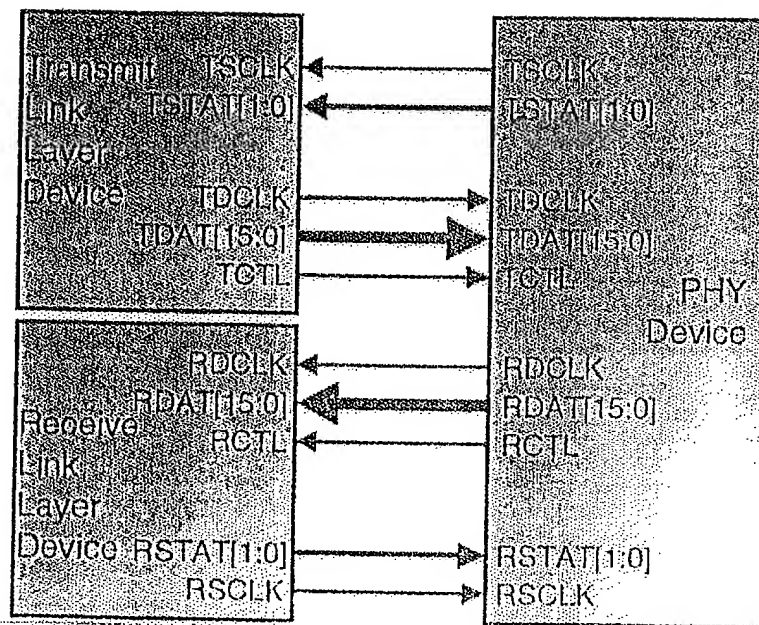


FIG. 9

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1	Byte 1	Byte 2		
Data Word 2	Byte 3	Byte 4		
Data Word 3	Byte 5	Byte 6		
Data Word 4	Byte 7	Byte 8		
Data Word 21	Byte 41	Byte 42		
Data Word 22	Byte 43	XX		

FIG. 10

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1	H1			H2
Data Word 2	H3			H4
Data Word 3	P1			P2
Data Word 4	P3			P4
Data Word 25	P45			P46
Data Word 26	P47			P48

FIG. 11

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1	H1			H2
Data Word 2	H3			H4
Data Word 3	HEC			UDF
Data Word 4	P1			P2
Data Word 5	P3			P4
Data Word 26	P45			P46
Data Word 27	P47			P48

FIG. 12

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1	Extended Address			
Data Word 2	Byte 1		Byte 2	
Data Word 3	Byte 3		Byte 4	
Data Word 4	Byte 5		Byte 6	

FIG. 13

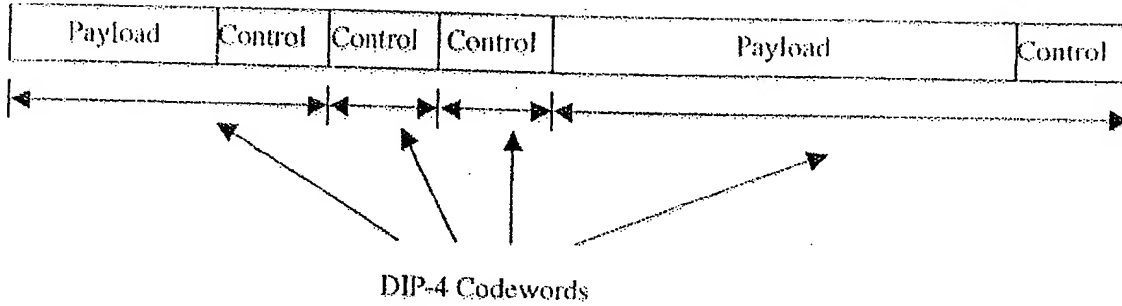


FIG. 14

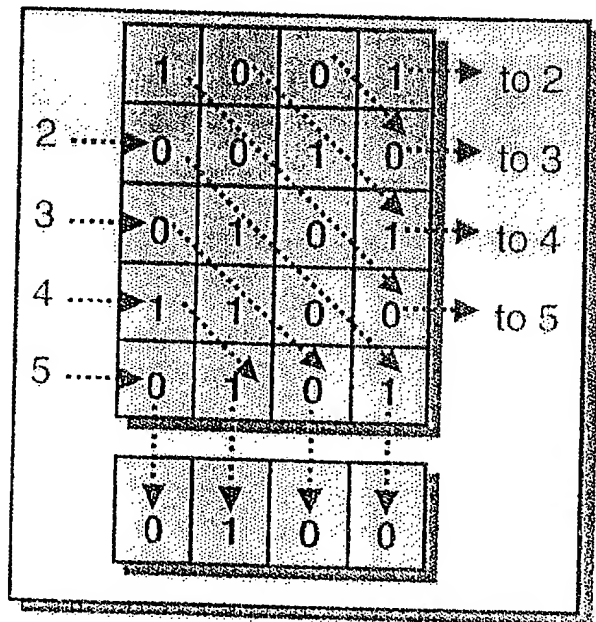


FIG. 15

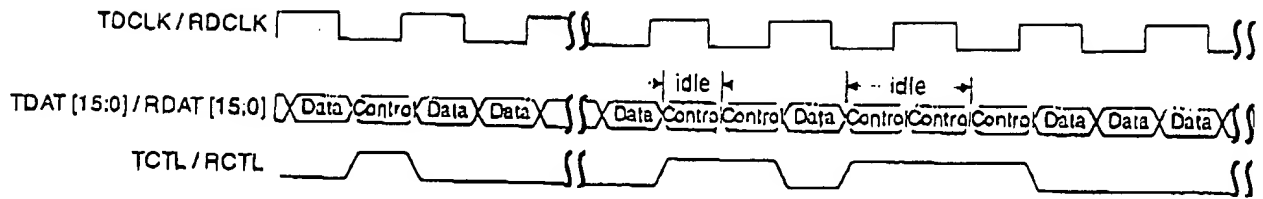


FIG. 16

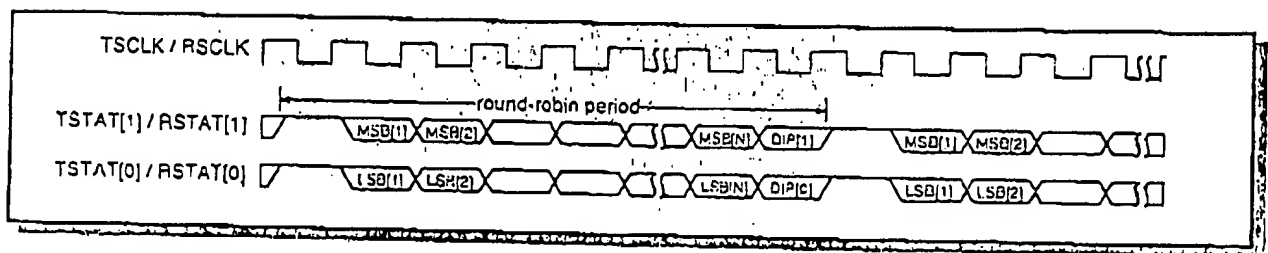


FIG. 17

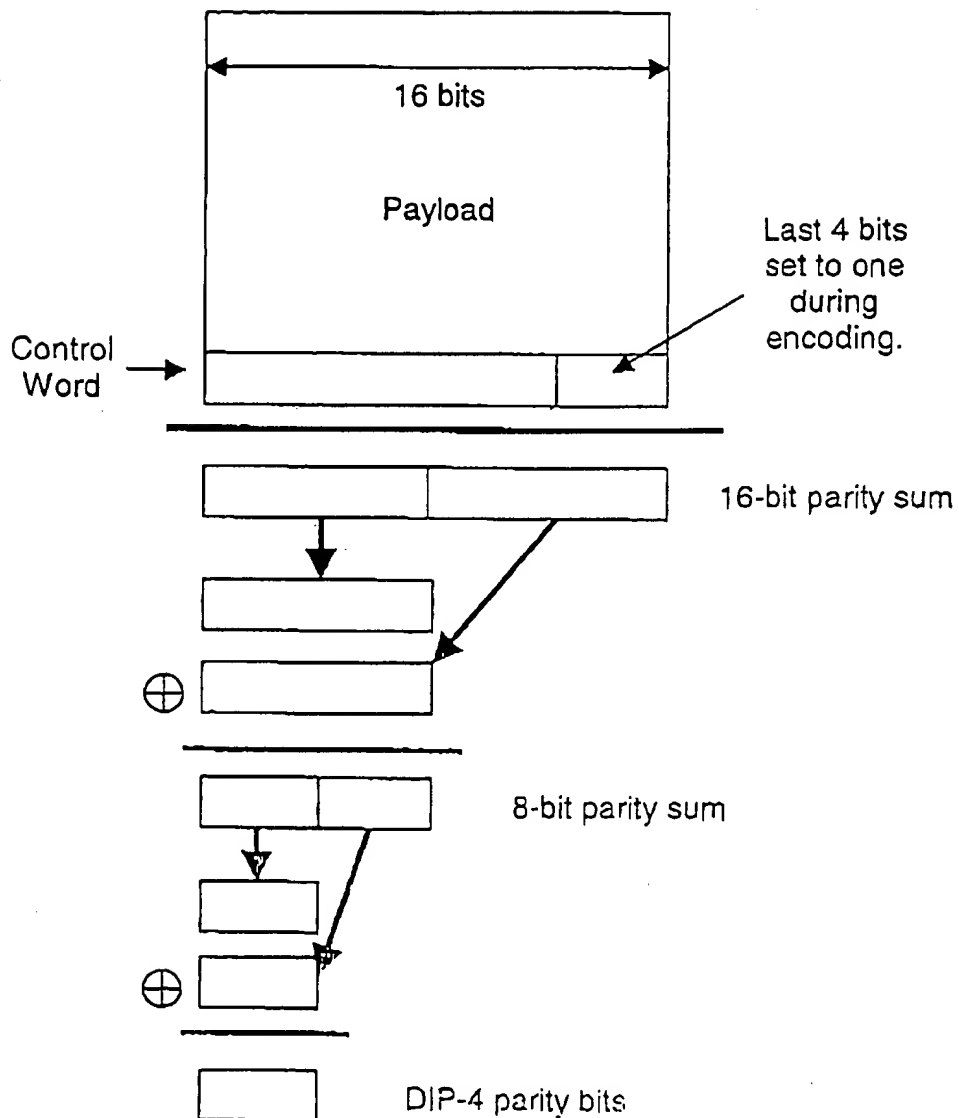


FIG. 18

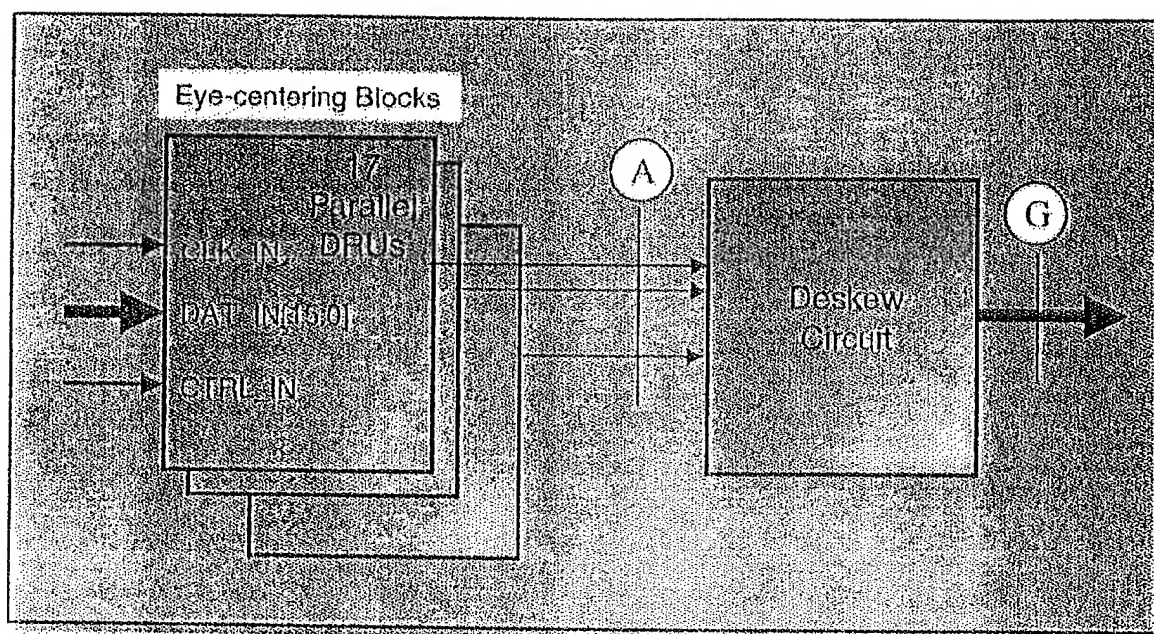


FIG. 19

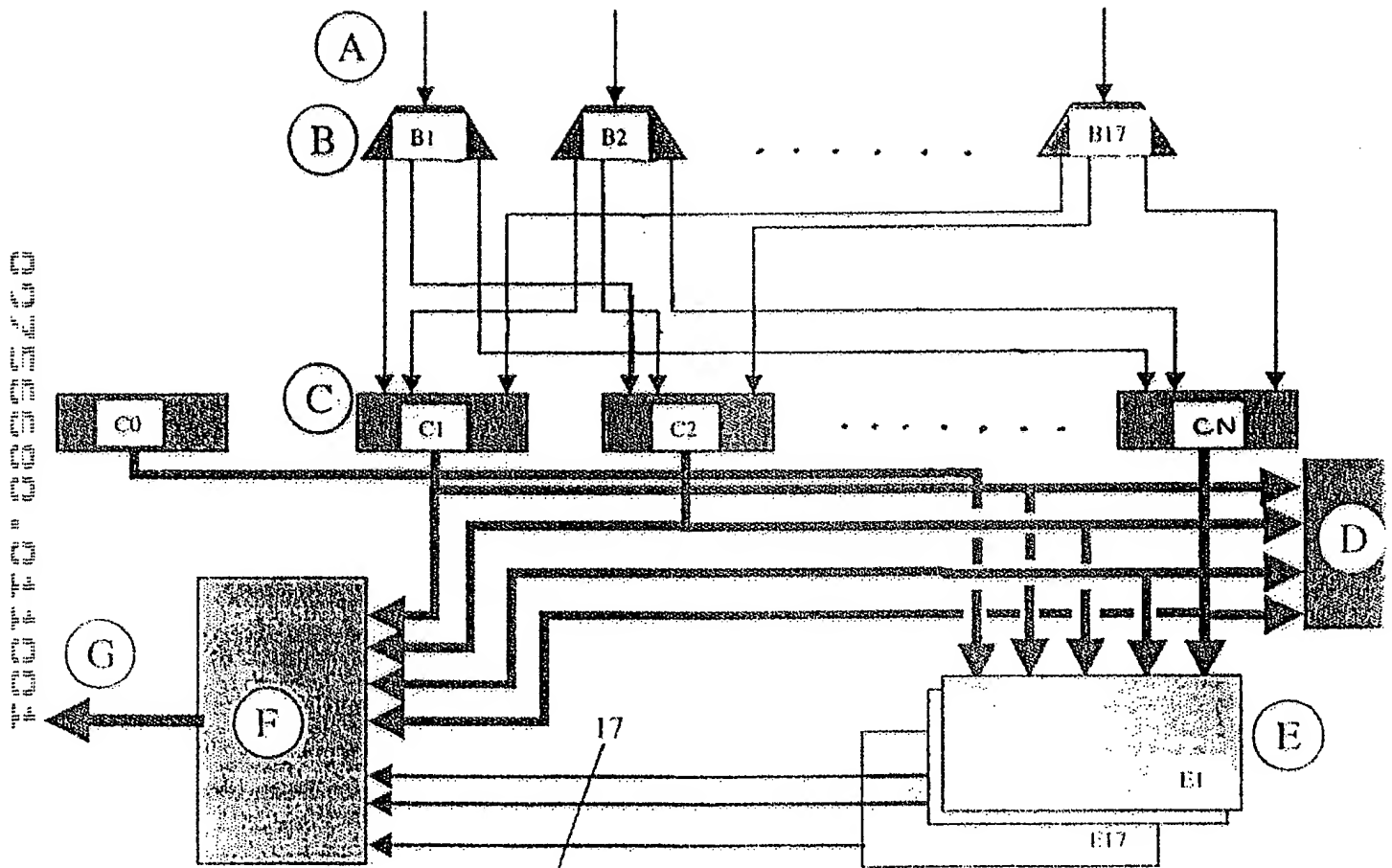


FIG. 20

